

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Li Attorney Docket: 2003 P 54309 US
Filed: Herewith Examiner: TBD
Serial No.: TBD Art Unit: TBD
For: Transistor with Silicon and Carbon Layer in the Channel Region

Mail Stop: Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:


The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

December 30, 2003
Date

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Respectfully submitted,



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Reg. No. 38,495

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	TBD
				Filing Date	HEREWITH
				First Named Inventor	Li
				Art Unit	TBD
				Examiner Name	TBD
Sheet	1	of	1	Attorney Docket Number	2003 P 54309 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-6,621,131 B2	09-16-2003	Murthy, <i>et al.</i>	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	2	ERNST, T., <i>et al.</i> , "A New Si:C Epitaxial Channel nMOSFET Architecture with Improved Drivability and Short-Channel Characteristics," 2003 Symposium on VLSI Technology Digest of Technical Papers, 4-89114-035-6/03.	

Examiner Signature		Date Considered	
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